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	FOF	RM		First Named	Inventor	Harry V. P	aul					
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(to be used for all correspondence after initial filing)				Examiner Name Clifford H. K			Knoll	(noll				
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Mary S. Keller

Typed or printed name

2-22-05)

Date





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Harry V. Paul	Examiner:	Clifford H. Knoll
Serial No.:	09/922,591	Group Art Unit:	2112
Filing Date:	August 3, 2001	Docket No.:	2864
Title	Fibre Channel Architec	ture	

Date of Deposit: 2-22-05

I hereby certify that this paper is being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450

Signature: Mary 5-Keller

Printed Name: Mary S. Keller

RESPONSE TO FINAL OFFICE ACTION

Commissioner for Patents Alexandria, VA 22313

This is responsive to the outstanding final Office Action mailed November 19, 2004. Reconsideration and allowance of this application is respectfully solicited in view of the following remarks. In summary, the Applicant believes that the claimed invention differs from the prior art in that the claimed invention creates a single, higher port count switch using switch modules and input/output modules that reside on different backplanes, while the prior art uses a backplane to connect multiple switches together in a multi-switch fabric.

REMARKS

Pending Claims:

In this application, claims 1, 6-27, and 34-41 are currently pending.

Rejection under 35 U.S.C. §102(e): Claims 1-27

The Examiner has rejected the pending claims as being anticipated by Carvey, U.S. Patent No. 6,606,656, with inherent features as evidenced by Dally, U.S. Patent No. 6,370,145. Carvey shows multiple modules (such as fabric routers) connected over a backplane together in a multi-module system. (Col. 6, lines 3-9). This can be seen in Figures 1 and 2 of Carvey, which show the modules (24 & 26) of Carvey being